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APPLICATION NO.	APPLICATION NO. FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/758,675	09/758,675 01/11/2001		Klaus Gloeckler	kler 10191/1639 9544		
26646	7590	03/08/2004		EXAMINER		
KENYON		ON	TORRES, JOSEPH D			
ONE BROADWAY NEW YORK, NY 10004				ART UNIT	PAPER NUMBER	
	•		,	2133	ld	
			•	DATE MAILED: 03/08/200	DATE MAILED: 03/08/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

PTO-90C (Rev. 10/03)

		Application No.	Applicant(s)	
		09/758,675	GLOECKLER ET AL.	• /
•	Advisory Action	Examiner	Art Unit	
		Joseph D. Torres	2133	
	The MAILING DATE of this communication appe			=
There final recondi	REPLY FILED 26 February 2004 FAILS TO PLACE efore, further action by the applicant is required to average to a subjection under 37 CFR 1.113 may only be either: (1) tion for allowance; (2) a timely filed Notice of Appeal nination (RCE) in compliance with 37 CFR 1.114.	THIS APPLICATION IN COND roid abandonment of this applica a timely filed amendment which	ITION FOR ALLOWANCE ation. A proper reply to a n places the application in	
a)	The period for reply expiresmonths from the mailing			
b) Efee harfee und (2) as	_	Advisory Action, or (2) the date set forth ater than SIX MONTHS from the mailing FILED WITHIN TWO MONTHS OF The date on which the petition under 37 CF of extension and the corresponding amount the shortened statutory period for reply the later than three months after the mail	g date of the final rejection. HE FINAL REJECTION. See MP R 1.136(a) and the appropriate e unt of the fee. The appropriate e originally set in the final Office ac	xtension extension etion; or
1.	A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37 CFF			
2.🛛	The proposed amendment(s) will not be entered be	ecause:		
(a	a) $oxtimes$ they raise new issues that would require furthe	er consideration and/or search (s	see NOTE below);	
	b) I they raise the issue of new matter (see Note b	·	,	
(0	they are not deemed to place the application in issues for appeal; and/or	n better form for appeal by mate	rially reducing or simplifying	g the
(0	i) 🔲 they present additional claims without canceli	ng a corresponding number of fi	nally rejected claims.	
	NOTE: See Continuation Sheet.	*		
3.	Applicant's reply has overcome the following reject	ion(s):		
4.	Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	be allowable if submitted in a se	eparate, timely filed amend	ment
5.🛛	The a) ☐ affidavit, b) ☐ exhibit, or c) ☒ request for application in condition for allowance because: See		dered but does NOT place	the
6.□	The affidavit or exhibit will NOT be considered becaraised by the Examiner in the final rejection.	ause it is not directed SOLELY to	o issues which were newly	
7.🖂	For purposes of Appeal, the proposed amendments explanation of how the new or amended claims wo			
	The status of the claim(s) is (or will be) as follows:			•
	Claim(s) allowed:			
	Claim(s) objected to:			
	Claim(s) rejected: <u>1,3-10 and 12</u> .			
	Claim(s) withdrawn from consideration:			
8.🖾	The drawing correction filed on <u>26 February 2004</u> is	s a) 🖾 approved or b) 🖂 disap	proved by the Examiner	
	Note the attached Information Disclosure Statemen		•	
	Other:	(3), 1 10 1770) 1 apel 140(3)	·	
	Otion			
	•			



Continuation of 2. NOTE: The Applicant contends, "The Examiner has objected to claims 1 and 3 9 on the grounds that in the phrase "activating the JTAG interface of the microprocessor with a test routine that is executable on the microprocessor and then transmitted to the JTAG interface" is ambiguous since no action is carried out prior to the "then" clause and it is unclear after what action the test routine is transmitted. Without passing judgment on the merits of this objection, claim 1 has been amended to recite "transmitting a test data stream provided by the test routine to the JTAG interface."

Newly added language would require another search.

Continuation of 5. does NOT place the application in condition for allowance because: The Applicant contends, "As discussed in the Amendment responsive to the previous Office Action, Margolis does not in any way refer to a boundary scan procedure according to IEEE 1149, let alone refer to specific ways of implementing such a procedure". The Examiner asserts that that a JTAG interface such as the one taught in Figure 1 of Argade is an interface for controlling a boundary-scan procedure according to IEEE 1149.

ORY PATENT EXAMIN

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